





NOTE: THIS EXISTING ELECTRICAL DRAWING IS ISSUED TO SHOW THE EPO SHUNT TRIP WIRING FOR BREAKER #624A1A SERVING PANEL 624A1A1A IN **ROOM 1275.** 





J	_	_	-	1/7/10	AS BUILT
Н	VDA	MCG	MCG	9/30/03	AS BUILT
G	VDA	MCG	MCG	8/11/03	ADD (2)-225A PANELS (#FD3027)
F					??
Ε					??
D	FWD	MCG	MCG	4/10/99	ADD & REMOVE SHUNT TRIPS
С	FWD	MCG	MCG	1/25/99	AS BUILT
В	ROB	MCG	MCG	11/20/98	ADD UPS-2, CORRECT UPS-1
REVISION NUMBER	DRAWN BY	CHECKED BY	APPR'D BY	DATE	REMARKS

50B NERSC MODIFICATIONS SUPERCOMPUTER SHUNT TRIP CONTROL WIRING DIAGRAM

DATE 11/11/1996						
8/23/1996						
8/23/1996						
CAD FILE PATH						
SHEET						
<b>E6</b>						

PROJECT NO. FX9010